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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.	
10/709,461	05/06/2004	Chi-Ming Hsiao	MTKP0067USA1 3460		
27765	7590 12/05/2005		EXAMINER		
	IERICA INTELLECT	LAM, TUAN THIEU			
P.O. BOX 506 MERRIFIELD, VA 22116			ART UNIT	NIT PAPER NUMBER	
	,		2816		

DATE MAILED: 12/05/2005

Please find below and/or attached an Office communication concerning this application or proceeding.

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		Application No.		Applicant(s)	<del></del>
		10/709,461		HSIAO ET AL.	
Office Action Sur	nmary	Examiner		Art Unit	
		Tuan T. Lam		2816	
The MAILING DATE of the Period for Reply	is communication app	ears on the cover sh	eet with the co	rrespondence ad	ldress
A SHORTENED STATUTORY WHICHEVER IS LONGER, FR - Extensions of time may be available under after SIX (6) MONTHS from the mailing described if NO period for reply is specified above, the second of the	OM THE MAILING DA er the provisions of 37 CFR 1.13 ate of this communication. he maximum statutory period v period for reply will, by statute, to three months after the mailing	ATE OF THIS COMN 36(a). In no event, however, vill apply and will expire SIX ( 1, cause the application to bec	MUNICATION may a reply be time  6) MONTHS from the come ABANDONED	In the second	,
Status					
1) Responsive to communic	ation(s) filed on <u>20 N</u>	ovember 2005.			
2a) This action is <b>FINAL</b> .		action is non-final.			
3) Since this application is in	n condition for allowar	nce except for formal	matters, pros	secution as to the	e merits is
closed in accordance with	n the practice under E	x parte Quayle, 193	5 C.D. 11, 453	3 O.G. 213.	
Disposition of Claims					
4)	is/are withdravowed. 2 <u>4-31 and 33-38</u> is/are ected to.	vn from consideration	n.		
Application Papers		·			
·	and An Invidence Property				
9) The specification is object 10) The drawing(s) filed on <u>06</u> Applicant may not request the Replacement drawing sheet 11) The oath or declaration is	S May 2004 is/are: a) and any objection to the cases including the correction	☑ accepted or b)☐ drawing(s) be held in a ion is required if the dra	beyance. See awing(s) is obje	37 CFR 1.85(a). cted to. See 37 CF	, ,
Priority under 35 U.S.C. § 119			•		
<ul><li>2. Certified copies of</li><li>3. Copies of the certified</li></ul>	None of: the priority documents the priority documents ied copies of the prior a International Bureau	s have been received s have been received ity documents have l (PCT Rule 17.2(a)).	d. d in Application been received	n No I in this National	Stage
Attachment(s)  1) Notice of References Cited (PTO-892	<b>)</b>	4) ☐ inter	view Summan (6	PTO-413)	
2) 🔲 Notice of Draftsperson's Patent Draw	ing Review (PTO-948)	Pape	view Summary (F er No(s)/Mail Date	e	
<ul><li>Information Disclosure Statement(s) ( Paper No(s)/Mail Date</li></ul>	PTO-1449 or PTO/SB/08)	5)  Notice 6) Othe		ent Application (PTC	)-152)

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## **DETAILED ACTION**

This is a response to the amendment filed on 11/20/2005. Claims 1-2, 4-12, 14-22, 24-31 and 33-38 are pending. The finality of the Office action dated on 9/18/2005 has been withdrawn in view of new grounds of rejection as follows:

## Claim Rejections - 35 USC § 103

- 1. The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:
  - (a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negatived by the manner in which the invention was made.
- 2. Claims 1-2, 4-12, 14-22, 24-31 and 33-38 are rejected under 35 U.S.C. 103(a) as being unpatentable over Baird (USP 6,753,738) in view of Carroll (USP 5,130,571), prior art of record.

Figure 8 of Baird shows an oscillator circuit comprising a plurality of switched capacitor circuits (302.0, 330.0). Each switched capacitor circuit comprises a single positive side switch transistor (302.0) and a capacitor (300.0) and a corresponding negative side switch transistor (303.0) and a capacitor (301.0), center switch (368 shown in figure 15). The single switch transistor selectively coupled a positive/negative side first node to a positive/negative side second node depending upon a control signal (Vsd <0>) and a low pass filter (capacitor Cload shown in figure 9) for making positive side switch elements gradually off.

Figure 8 of Baird shows a single switch transistor instead of a plurality of differently sized switch transistors sequentially switched off with a smallest switch transistor being switched off last as called for in claims 1-2, 4-12, 14-22, 24-31 and 33-38.

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Figure 4 of Carroll reference discloses a switched capacitor circuit having a plurality of differently sized switch transistors (T24, T22, column 3, lines 1-5). The transistors are sequentially switched off with the smallest (T22) being switched off last to obtain a fast acquisition while maintaining a low offset voltage. Therefore, it would have been obvious to a person skilled in the art at the time the invention was made to replace applicant's single positive and negative side switch transistor shown in Baird with a plurality of differently sized switch transistors sequentially switched off with a smallest switch transistor being switched off last for the purpose of increasing speed with a minimal offset voltage.

## Conclusion

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Tuan T. Lam whose telephone number is 571-272-1744. The examiner can normally be reached on Monday to Friday (7:30 am to 6:00pm).

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, TIMOTHY P. CALLAHAN can be reached on 571-272-1740. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

Tuan T Lam

Primary Examiner

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12/1/2005